

THE METAL- SEMICONDUCTOR INTERFACE

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INTRODUCTION

Interfaces between metal and semiconductor may be found almost everywhere in contemporary electronics. Often the metal is there just to serve as a contact to p-n junctions in the semiconductor. At other times, the metal-semiconductor interface itself performs essential electronic functions. Considerable scientific interest has been devoted to this latter situation since early in the century, as discussed by Welker (1) in the previous volume of this series. This early work led to a rather simple and classical model, in which an electrostatic barrier ϕ arises within the semiconductor and produces the rectifying behavior. The barrier ϕ is called the Schottky barrier or Schottky-Mott barrier in remembrance of that work. The prediction of ϕ has proven not to be so simple, however, whether in terms of other phenomena (such as work functions) or in terms of fundamental theories. It is to the various contemporary aspects of this problem that the present review is principally devoted.

The most general treatments of the subject appear in the well-known 1957 book of Henisch (2) and in a more recent one by Rhoderick (3). Much of the current activity is reflected in the *Proceedings of the Annual Conferences on the Physics of Compound Semiconductor Interfaces*, usually published in the *Journal of Vacuum Science and Technology*, in addition to the standard physics journals. As for the chemical literature, there is but an occasional foray between the surface chemists and the practitioners of the metal-semiconductor interface: this in spite of the considerable emphasis on interface chemistry in recent discussions of the Schottky

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barrier. An effort is made in the present review to help bridge this gap by mention of some of the relevant accomplishments of surface chemistry.

This review consists of two parts. The first deals with various interfacial phenomena. Most fundamental among these is the interfacial energy, a thermodynamic property seldom measured in the present context. Electrostatics of the solid-vacuum interface is treated next, because it has provided so much of the data for phenomenological correlations with ϕ . The property of actual solid-solid interfaces receiving the most study today may well be structure, which we then consider. Finally, much of the current work on ϕ is treated.

The second part of the review deals with theories of the metal-semiconductor interface, most of which have tried to explain the differences in origin of the states pinning the Fermi level in the covalent materials and the lack of pinning in the more ionic materials. Many of the early theories concentrated on ideal models of the metal-semiconductor interface. However, more recently the theories have turned to the role of structure at the interface, including defects, in producing the states responsible for pinning the Fermi level.

PHENOMENA AT THE INTERFACE

Thermodynamic Properties

Thermodynamic properties are usually regarded as the most fundamental of macroscopic properties. Where surfaces are involved, and even more so interfaces, the experimental difficulties have often been prohibitive, however. This has generally been the case for semiconductors and the metal-semiconductor (M-SC) interface (4). Recently, the situation has begun to change, however, particularly as a result of careful observations made during crystal growth (5–10). Furthermore, the enormous improvements in ultra high vacuum (UHV) techniques in recent years could permit suitable control of semiconductor surfaces for thermodynamic characterization. While it is true that present UHV apparatus was developed primarily for atomistic studies of surfaces, some thermodynamic characterization could readily be accomplished in the course of operating such facilities.

First, consider what is well known about the relevant interface energies, which are perhaps better known as “surface tensions.” The best characterized of these interfaces is that between liquid metal and its vapor. The free energy or surface tension, σ_{LV} , for this interface appears in several tabulations (11–13), having become rather well defined at least for the more common metallic elements. The customary units are $\text{ergs/cm}^2 = \text{mJ/m}^2$, and it is sometimes useful to bear in mind that

1000 mJ/m² typically corresponds to ~ 0.6 eV per surface atom exposed. The magnitude of σ_{LV} is usually larger for metallic elements than other substances (13), particularly so for the transition elements. The latter have $\sigma_{LV} \gtrsim 1000$ mJ/m² and indeed, for the most refractory of these, $\sigma_{LV} \gtrsim 2000$ mJ/m². As one progresses from left to right across the periodic chart of the elements, however, σ_{LV} takes a sharp drop in the region where the common semiconductors form. This region is shown in Figure 1, and includes the elements (except for Be) that do not form compounds with Si. Figure 1 shows these elements rather arbitrarily divided into three groups: elements with high surface tension ($\sigma_{LV} > 600$ mJ/m²) to the left; an intermediate group ($600 > \sigma_{LV} > 300$) in the middle; elements with low surface tension to the right. The σ_{LV} values are for the liquid element near its melting point and are drawn from References 12–14 in order of descending preference. Generally σ_{LV} declines rather slowly as temperature is increased. Values for Si and Ge are not shown in the figure, because they are not known with comparable accuracy. The various data for Ge have been discussed and an average value $\sigma_{LV} = 616$ mJ/m² suggested (6). A value for Si of $\sigma_{LV} = 720$ mJ/m² has been stated without comment (10).

Surface tension between solid and vapor, σ_{SV} , is considerably harder to measure and also is subject to some well-known complications (15). The available data have been analyzed in conjunction with some empirical rules regarding heats of formation; results are tabulated for a dozen or so elements (11, 12). For each of these, σ_{SV} is larger than σ_{LV} , the excess ranging from ~ 10 –30 % of σ_{LV} . No σ_{SV} values for Si or other semiconductors appear to be reported.

Surface tension between semiconductor and metal, σ_{SL} , would be the hardest of all to measure directly. Considerable progress is being made,

		Al 870	Si	P	S
Cu 1258	Zn 750	Ga 735	Ge	As	Se 100
Ag 926	Cd 609	In 559	Sn 549	Sb 369	Te 170
Au 1128	Hg 476	Tl 446	Pb 448	Bi 380	Po

Figure 1 Surface tensions, in mJ/m², are shown for elements near Si in the periodic chart.

however, through contact angle measurements. The schematic of Figure 2 recalls for the reader the way in which three phases converge to form the contact angle θ . Equilibrium considerations lead to Young's equation (16)

$$\sigma_{SV} - \sigma_{SL} = \sigma_{LV} \cos \theta. \quad 1.$$

Since σ_{LV} is relatively well known, as just discussed, measurement of θ defines the right-hand side of Equation 1. Thus one obtains a value of σ_{SL} compared to σ_{SV} , if not a value for σ_{SL} itself.

Until recently, the contact angle between a crystal and its own melt was assumed to be zero. Work by several investigators (5–10) now confirms that such is not the case for Si and Ge, where a contact angle of the order of 10° occurs. Incidentally, the old assumption that $\theta = 0$ evidently does apply to metals like Cu (7) and Ga (8). Since liquid Si and Ge are for most purposes metallic, the result $\theta \sim 10^\circ$ may be considered the first measurement of a contact angle for a M-SC interface. Other combinations of metal and semiconductor can readily be investigated with contemporary UHV facilities. Figure 3 shows the small contact angle exhibited by In on (100) Si in experiments performed in the authors' laboratory. For comparison, the large θ for In on SiO_2 appears in the left half of the figure. The substrate, exposing SiO_2 and Si, was subjected to 2-keV Ar^+ sputtering but was not annealed, and a moderate vacuum, baseline approximately 10^{-9} Torr, obtained. When the substrate is not sputtered, but only cleaned by conventional chemical methods, a large θ develops for In on Si, similar to that for In on SiO_2 in the figure.

Such measurements for a variety of metals on the common semiconductors could provide a more direct account of the overall energy associated with the M-SC interface than is presently available. Contact angle measurements have been pursued for other substrates with some profit. A notable example is the work of Zisman (17) on low energy

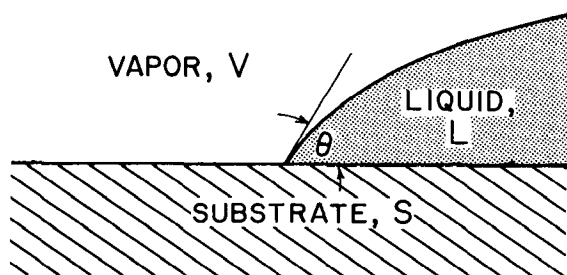


Figure 2 The contact angle θ is the arc occupied by liquid around the point of convergence of solid, liquid, and vapor.

surfaces, mostly polymers. Homologous series of organic liquids contacting a given polymer were found to exhibit a linear relation between $\cos \theta$ and σ_{LV} , thus enabling one to define a critical surface tension for each substrate corresponding to $\theta = 0^\circ$. The critical surface tension was associated with the molecular group exposed; CF_3 endings, for example, give the lowest of all critical surface tensions, some 6 mJ/m^2 . Perhaps quite a different relationship may apply to the M-SC interface.

In the absence of data on interfacial energies, resort has been made to bulk energies. The latter was invoked in various ways in correlations of Schottky-barrier heights, e.g. via the Pauling electronegativity and more directly in the work of Andrews & Phillips (18).

Work Function and Electronegativity

The original concept of the Schottky barrier invoked the work functions of the two substances constituting the interface to predict the barrier height. While this simple procedure has not in general proven sufficient, the work function, W_F , and related parameters remain of primary im-

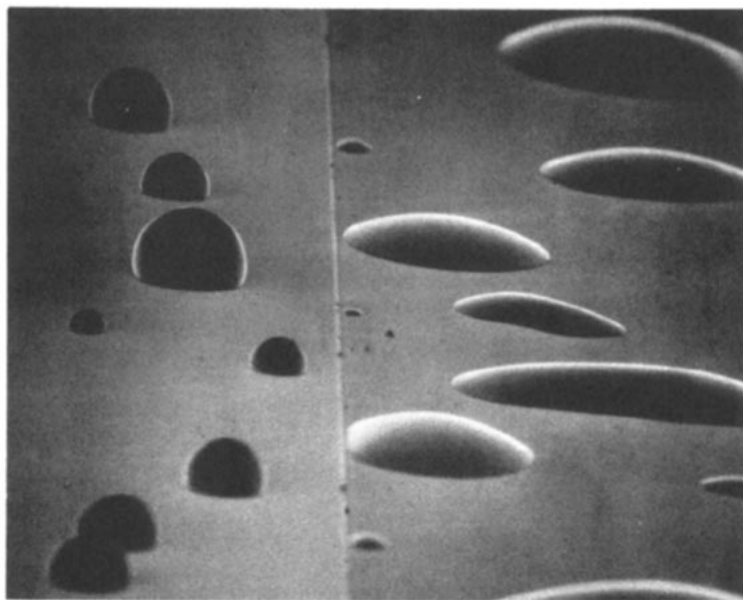
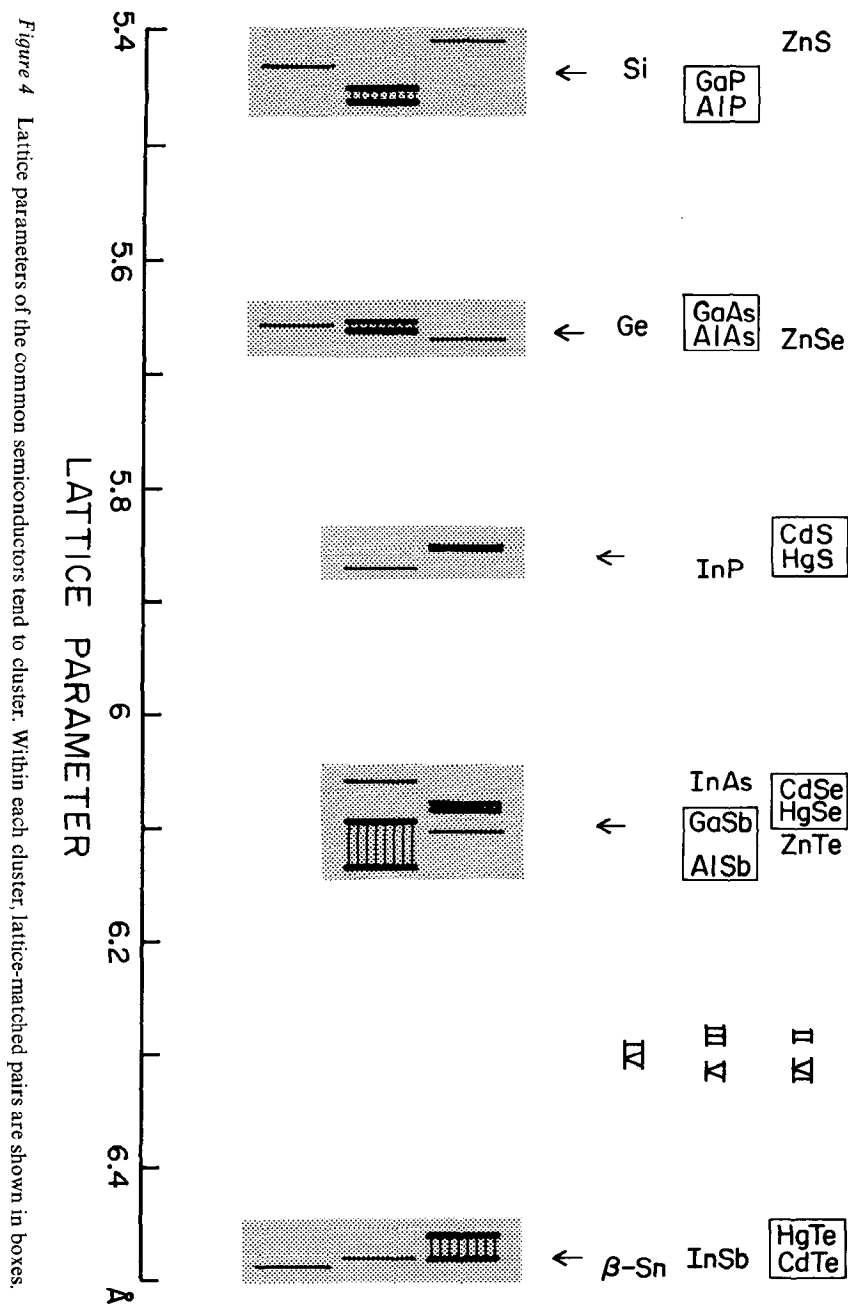


Figure 3 Scanning electron microscope view, at grazing incidence, of the configurations adopted by an In film upon melting on SiO_2 (left) and on Si (right). The largest blob on the left has a diameter of $\sim 25 \mu\text{m}$.



portance in both theoretical and phenomenological treatments of the barrier. The accepted values of W_F do change with time, however. Comparison of the Michaelson compilations of W_F made in 1950 (19) and 1978 (20) indicates some shifts as large as 0.5 eV. Consequently, electronegativity, which is founded in better characterized phenomena, has often been used in place of W_F in experimental correlations of barrier height. The Pauling electronegativity has undergone some changes, however: the 1975 values (21) for six elements differ from the 1960 values (22).

Structural Properties

At first glance one might expect the semiconductor interface with vacuum to be the simplest starting place from which to view the formation of M-SC structures. In fact, however, this interface is complicated by reconstruction of the surface. For example, LEED observations show that a multiplicity of reconstructions occur on various GaAs faces (23). The determination of atom positions in such reconstructions has received much attention, but frequently presents a rather formidable problem. On (111) Si surfaces, the 2×1 structure appears to be better understood than the rather complex 7×7 structure (24–28). For (100) Si, recent results disagree with proposed geometrical models (29). In some respects the situation for GaAs, and perhaps most III-V and II-VI semiconductors, appears simpler. There is general agreement, for example, that (110) GaAs relaxes by moving surface As atoms outward and Ga inward (30). Recent work is devoted to determining the amount of the tilt angle by which As rotates outward (31).

Once metal atoms are introduced to the semiconductor surface, a number of events can occur. Perhaps the simplest is for the added atoms to continue the bulk crystal structure of the substrate, as reported for Al on GaAs (32) and Au on GaAs (33), for example. Reaction with the substrate is a further possibility, either by simple interchange (34) or perhaps involving defects. Since bonds must be broken, both temperature and structural defects (35) are likely to play a role.

In any event, one eventually lays down substantial metal upon the semiconductor and this final structure is studied in its own right. Such studies have probably proceeded furthest in the case of SC-SC interfaces, the so-called heterostructures. This work, recently reviewed by Olsen & Ettenberg (36), deals with bulk analogs of the subjects mentioned above: dislocations instead of point defects, strain instead of atom displacement, etc. In fact, it has developed to a rather sophisticated state. Quaternary systems can be laid down layer by layer in a lattice-matched condition. For the III-V materials primarily investigated in such work, it appears

that the better the lattice match, the better the electronic behavior of the interface (36).

Some of the lattice-matched heterostructures are also Schottky barriers. This comes about because the band gap, E_g , goes to zero for some compositions among the II-VI compounds, so that one side of the heterostructure approaches the behavior of a metal in a M-SC structure. The room temperature lattice parameters of the common semiconductors are displayed in Figure 4, where they can be seen to cluster in five groups. Three of these are based on the elemental semiconductors, Si, Ge, and β -Sn, and the other two are interrow combinations. Within the clusters are some closer matches whose lattice parameters are given in Table 1. Most of these close lattice matches depend on size equivalence between the cations Al-Ga and Cd-Hg. Thus, for example, AlAs-GaAs or CdSe-HgSe tend to be lattice-matched. In the latter example, the zero-gap compound HgSe occurs and thus confers on this pair characteristics of a Schottky barrier. The lattice-matched Schottky barriers CdX-HgX, where X is a chalcogen, are discussed in the section on Schottky barriers.

Electrical Properties

Electrical behavior at M-SC interfaces is dominated by the electrostatic barrier, ϕ . The way in which this barrier controls electrical properties has been discussed many times (38); here we discuss only the physical, or chemical, origin of ϕ . Only limited agreement exists today on this subject, and some investigators even feel that understanding will be attained only substance by substance: "a 'general' theoretical model valid for all the metal-semiconductor interfaces appears a more and more difficult goal . . ."

Table 1 Closely lattice-matched pairs of binary semiconductors

Semiconductor pair	Lattice parameter (Å)	Reference	Mismatch (%)
CdS	5.8503 ^a	37	~0.01
β -HgS	5.851		
CdSe	6.079 ^a	37	0.08
HgSe	6.084		
GaAs	5.6534	36	0.126
AlAs	5.6605		
CdTe	6.481	37	0.32
HgTe	6.460		

^a Where hexagonal crystal structures occur, the lattice parameter quoted is for basal plane matching with the cubic structure.

(39). As for the experimental situation, the list of parameters that can affect the barrier is increasing.

With so many uncertainties attending the subject, are any generalizations possible? Probably most investigators today would still agree that barriers on semiconductors like silicon are relatively insensitive to the choice of metal, whereas the more ionic semiconductors, e.g. ZnS, show wider variation in ϕ with the choice of metal. Whether or not the transition between these behaviors is sharp, which experimental ϕ 's are to be preferred, etc, are questions subject to dispute. Various aspects of the experimental definition of ϕ are treated in this section.

A fundamental problem has been present all along: Is the barrier uniquely defined by the materials forming it? Let ϕ_{MS} be the barrier between metal and semiconductor. For ϕ_{AlSi} , the widely used combination used in the integrated circuit industry [e.g. in transistor-transistor logic (TTL) "Schottkies"], a range of values has been known for many years to occur. The range can exceed one-quarter volt for common processing conditions and, indeed, various proprietary treatments have been used to stabilize ϕ_{AlSi} . In this particular example, impurities at the interface are likely involved, though this is presumably not so in other instances. The p-InP/n-CdS heterojunction, somewhat analogous to a Schottky barrier, has been prepared by the cleanest methods available today. Yet the voltage offset, in this case ΔE_c for the conduction band, depends on the preparative method selected to the extent of ~ 0.5 eV (40,41). As we review recent Schottky-barrier measurements, it becomes evident that several parameters must be specified to fix ϕ . To illustrate the magnitude of this problem, however, we first highlight literature on interfaces that appear to exhibit no barrier.

OHMIC CONTACTS For a substance like ZnS, which has $\phi \gtrsim 0.8$ eV for all metals studied (42) and which cannot be heavily doped to induce tunneling contact, one wonders how the interior of the substance can be made accessible to electrical contact. Early work on this material obtained ohmic contact by etching in 250°C pyrophosphoric acid, followed by scribing on In amalgam and by firing in H_2 at 350°C (43). Thus the apparently high values of ϕ were circumvented. Incidentally, In amalgam scrubbed in at room temperature yields good ohmics on a wide variety of semiconductor substrates (44) for reasons not yet elucidated.

Considerably more sophisticated ohmic contacts have been prepared during the past three or four years by workers utilizing UHV with associated spectroscopies. Williams and associates in a series of investigations on InP (45–47) find $\phi = 0$ for Al, Fe, and Ni deposited on clean (110) material, whereas these same metals give $\phi \sim 0.5$ eV when deposited

on etched (110) InP. Just the opposite occurs when the metals are Ag or Au. These give a ~ 0.5 eV barrier when deposited on clean (110) InP at room temperature, but give ohmic contact when one-half monolayer of oxygen or chlorine is present on the InP prior to metal deposition. On the other hand, Farrow (48) and Massies et al (49) find that even Ag on clean InP gives ohmic contact provided that the crystal face exposed is (100). These results suggest that interface chemical reaction is an important consideration in InP.

The literature on ohmic contacts is extensive if one includes, as we have above, data that are incidental to the main purpose of a paper. Tabulations of various recipes have been presented by Milnes & Feucht (50), as well as Rideout (51). We have cited above only a few examples, however, to illustrate the point that low barriers are quite achievable, whether by witchcraft or the most modern scientific methods.

SCHOTTKY BARRIERS After one examines the many recipes for making low barriers, it seems rather remarkable that high barriers can be made reproducibly, as they are in industry, especially since a small area of low barrier height in parallel would effectively represent a short circuit. On the other hand, ϕ seldom rises to the height of the band bending in a p-n junction, as has become somewhat painfully clear in the photovoltaic field. Cases of maximum barrier height, $\phi \approx E_g$, where E_g is the band gap, are rare. Examples are the n-type antimonides, p-InAs (52) and p-PbTe (53), most of which have small band gaps. A more common result (54) is $\phi \approx \frac{2}{3}E_g$. Larger effective ϕ , desirable in Schottky-barrier solar cells, sometimes results from impurities deliberately introduced at the interface. Notable cases are thin oxides in Al/p-Si (55) and Au/n-GaAs cells (56).

The main new fact to emerge from the rather elegant UHV preparations of Schottky barriers during the past few years is the diversity that can be obtained. As pointed out above, ohmic contacts may be produced or not, depending on the crystal face presented by the substrate. Stoichiometry can be influential, and so can even the exact LEED pattern present on the substrate. A further important influence is temperature, which can promote interdiffusion as well as interfacial reaction, and is apt to be particularly important in the lower melting point compounds like InP. In view of this complexity, we group recent measurements of ϕ by substrate.

Silicon is still the most studied substrate if not the simplest. Roughly speaking, it remains true today that $\phi \approx \frac{2}{3}E_g$ for n-Si. The highest barrier reported (57, 58) is 0.93 eV for IrSi/n-Si, which amounts to $\sim 0.84 E_g$. The exact barrier height is of some importance in integrated circuits, where

Schottky barriers operate in conjunction with p-n junctions. Since transition metal silicide is often produced at the M-SC interface for metallurgical reasons anyway, the exact choice of transition metal can be made advantageously to fine-tune ϕ . The extensive studies of silicide-silicon systems have been reviewed by Van Gorp (59), Tu & Mayer (60), and Ottaviani (61). Also a short account appears in the book by Rhoderick (62).

Fundamental studies with Si substrates have proceeded with the full armament of spectroscopies and microscopies. Where silicide formation occurs, as just discussed, nucleation of the new phase often appears to dominate the kinetics (61), which are in any case rather complex (63). Metals that do not form bulk silicides offer a simpler prospect, although one should bear in mind that even in this case "two-dimensional" compounds may exist. For example, indirect evidence from MBE (64) suggests that such may be the case for Al on Si, and other evidence (65) indicates that an "intermediate" layer of some sort forms between Au and Si.

Extensive studies have been carried out with the non-silicide-forming metals: Al, Ga, In, Ag, and Au. A rather striking result is the insensitivity of ϕ to what occurs on the Si surface. The band bending that exists when the Si surface is bare does not change as Ag or Au is deposited (66). Neither does contamination by O₂, air, or chlorine appear to affect this result. On the other hand, Al, Ga, and In evidently introduce a shift of ~ 0.2 eV in the band bending (39). The modification that does occur in the band bending, however, takes place for small metal coverage, of the order of a monolayer. Even though ϕ does not change much as the Si surface is metallized, the electronic states in the system change substantially. These changes have been interpreted as replacement of intrinsic surface states by extrinsic interface states, and involve formation of interface bonds followed by formation of an intermediate region between metal and semiconductor (39).

Studies utilizing III-V compound substrates encounter a rather different set of problems. Stoichiometry becomes an issue and, in practice, diffusion across the interface is often a problem. An offsetting advantage, however, is the fact, first shown clearly by van Laar (67), that the clean substrate usually has no states in the band gap, i.e. "flat band" conditions prevail near the surface. Thus the development of band bending can be followed quite sensitively as metal is deposited on the surface and appears to be completed by ~ 0.1 monolayer coverage (35). Since these facts became evident a few years ago, a great deal of study has occurred and is only highlighted here.

GaAs has perhaps been studied the most. Conditions at the surface can be controlled very sensitively as metallization occurs. Thus single-crystal

Al can be grown on GaAs (100) to form a Schottky barrier (32). When the substrate is Ga-stabilized, ϕ is ~ 60 meV greater than when the substrate is As-stabilized. Silver contacts on (100) GaAs have been studied in some detail and show a similar sensitivity to surface stoichiometry, as well as to impurities (49). A recent proposal ascribes the pinning of the Fermi level, as the interface is formed, to anion deficiencies (68, 69). Some support for this proposal appears to be developing at this time (1979). Thus defects of different sorts may be important in the development of the interface.

If the microscopic defects just alluded to were to become extensive enough, one would expect to detect macroscopic phenomena like diffusion. Substantial low-temperature diffusion of components of compound semiconductors reportedly occurs through Au contacts (65). The rapid degradation of ϕ for AuGe contacts on GaAs during moderate heating (70) may be related to this phenomenon. In the other direction, diffusion of Al through GaAs has been reported to be quite rapid at $\sim 850^\circ\text{C}$ during LPE (71). On the other hand, interdiffusion of Al and Ga in GaAs is reportedly exceedingly slow, $D \sim 10^{-20}$ cm²/sec, during MBE (72). Similar discrepancies are reported for Ge/GaAs interdiffusion (73, 74). The question of diffusion near interfaces is far from resolved at this time.

Similar studies on InP are in progress. Stoichiometric effects on ϕ have been found (49), and interdiffusion can clearly affect ϕ (75). As was mentioned earlier, dramatic effects on ϕ arise from the choice of crystal face on which metal is deposited. InP, if anything, appears to be richer in such phenomena than GaAs.

The investigations just discussed focus attention on the behavior of a semiconductor as metal is added, the metal coming from a rather conventional repertoire extending from Al through Au. Metals more electro-positive than Al are generally too reactive to be of interest, but substances more electronegative, or "noble," than Au are potentially useful in this connection. Some of the phenomenological correlations to be discussed predict that the latter substances would give higher barriers on n-semiconductors than Au does. So far, two such substances have been investigated, polymeric sulfur nitride (76) and polyacetylene (77). Both produce high barriers; the former clearly produces higher barriers than Au.

Another approach to the attainment of higher barriers is possible with the lattice-matched Schottky barriers; CdX-HgX was mentioned in the section on structural properties. Based on W_F arguments or the "common anion" correlations discussed in the next section, one expects HgX to be effectively more electronegative than Au. Studies of CdSe-HgSe prepared by chemical vapor deposition (78) show $\phi = 0.73 \pm 0.02$ eV,

which is ~ 0.24 eV higher than occurs with CdSe-Au. Also noteworthy is the relatively small uncertainty in ϕ for this lattice-matched structure.

CORRELATIONS WITH OTHER PHENOMENA Accompanying the many measurements of ϕ have been attempts from time to time to find interrelations with other phenomena, either interfacial or bulk. Originally ϕ was expected to vary sensitively with W_F . In practice a smaller variation was found to occur and a correction factor S was introduced. The factor S came to be attributed to covalent or ionic character of the semiconductor side of the M-SC interface (79). Subsequently some features of this description, particularly the sharpness of the ionic-covalent transition, have been questioned (80), and agreement today is probably limited to the qualitative nature of the two regimes.

A later proposal, applicable to Au contacts on common semiconductor compounds, associated the barrier ϕ with the anion of the compound (81). This "common anion" rule met with some success in the case of ternary arsenic compounds (82), ternary phosphorus compounds (T. F. Kuech, unpublished observations), and InGaAsP quaternary compounds (83). It does not apply in other situations, e.g. to Al-containing compounds (84), where impurities are likely to occur at the interface. The proposal has been useful in suggesting means to increase barrier heights (85).

Subsequently, a scheme that classifies M-SC interfaces as reactive or nonreactive has been proposed (86). The decision as to reactivity is based on photoemission spectra, and the transition between the two classifications so far appears to be sufficiently sharp to make the distinction (87).

The correlations of most practical interest today are probably those for silicon-silicide interfaces. A proposal by Andrews & Phillips (18) based on heats of formation gave good agreement with the ϕ values then available. More recent measurements (57), however, have not conformed to the correlation. A scheme based on eutectic temperatures, however, appears to give good agreement with all the silicide barriers presently known (G. Ottaviani, unpublished data).

THEORIES OF THE SCHOTTKY BARRIER

Most of the theoretical studies of metal-semiconductor interfaces concentrate on explaining the relative independence of the barrier height on the metal in covalent semiconductors and the wide variation observed in ionic semiconductors. The theories date to the original idea of Bardeen (88) that pinning of the Fermi level on the covalent semiconductors (i.e. barrier

heights that are relatively independent of the metal) is due to the presence of surface states on the semiconductor which charge in such a way as to fix the Fermi level in the semiconductor relative to the valence band and conduction band edges. For the ionic semiconductors, only a few or no such states exist and the Fermi level is unpinned. A simple application of this concept to the electrostatics leads to a value for the slope S of the barrier height with electronegativity of the metal (89)

$$S = \frac{A}{1 + \frac{e^2}{\epsilon_0} D(\epsilon_F)(\delta_S + \delta_M)}, \quad 2.$$

where A is the slope of the work function versus electronegativity of the metal, $D(\epsilon_F)$ is the density of surface states, δ_S is the screened decay length for these states into the semiconductor, and δ_M is the Thomas Fermi screening length in the metal. Most of the theories to date are based on these ideas.

In the mid-1960s, Heine (90) pointed out that this point of view required some modification since the presence of the metal will turn most surface states on the semiconductor into states that extend throughout the metal and decay into the semiconductor. In recent years it has become clear that the states producing the pinning are not simply states that existed on the ideal semiconductor-vacuum interface. With the exception of a few of the covalent semiconductors (e.g. Si and Ge) (91, 91a,b), the ideal surface-vacuum interface of the covalent semiconductors does not possess surface states at the appropriate energy to pin the Fermi level (67, 92). Hence, the states responsible for the pinning must be due to deviations of the surface from the ideal (e.g. defects) or states introduced by the addition of the metal.

Theoretical studies (93–99) have been carried out for realistic models of the semiconductor and jellium models of the metal. The assumed semiconductor structure in these models is that obtained by simply terminating the perfect bulk. Since the metal is modeled by jellium, the interface possesses translational symmetry parallel to the interface and is much like the problem presented by a perfect semiconductor-vacuum interface. The sophistication of the calculations has ranged all the way from empirical calculations (93–95, 99) to self-consistent, pseudopotential calculations (96–98). The primary result of these calculations is the value for the density of states in the gap of the semiconductor and the appropriate decay length for these states. These parameters are then fed into Equation 2, which yields a value for S . All the theories seem to be able to explain the quantitative trend in S . That is, they find that S is typically

small for very covalent semiconductors and that S is rather large for more ionic semiconductors. The degree of agreement or disagreement depends strongly (100) on the value of A used in Equation 2. Flores et al (93–95) take $A = 1$ and claim poor agreement with experiment, while Louie et al (97) and Mele & Joannopoulos (99) use $A = 2.3$ and claim good agreement with experiment. Fits of a linear relation for the work function to the electronegativity of the metal for a recent compilation (101) of work functions suggest that A varies quite a bit for differing sets of metals. If all the metals in the compilation are used, then one obtains $A \simeq 1.8$. However, if only the metals typically used in Schottky-barrier studies are included (Al, Au, Ni, Mg, etc) (96, 96a), then a value of $A \simeq 1.0$ is obtained. This uncertainty in A , the questionable validity of the assumption that the work function of the metal is a linear function of the electronegativity of the metal, makes it difficult to decide whether or not these theories contain the major ingredients of an explanation of the Schottky-barrier phenomena.

Two of the theories disagree about whether the difference between covalent and ionic semiconductors is due to band-gap variations or due to covalency. Diamond is the important case in this discussion since diamond has a large band gap, about 5.5 eV, and yet is covalent. Mele & Joannopoulos (99) predict that diamond should have $S \approx 0$, while Ihm et al (98) predict that diamond should have $S \approx 0.4$. A very limited, old set of experimental results suggests that $S \approx 0$ (102) for diamond.

Louie and co-workers (96, 96a) calculated the value of the barrier height for Al on Si. They found that their model gives a value $\phi = 0.64$ in good agreement with the experimental value, $\phi \approx 0.75$ eV.

Inkson (103, 103a) has pointed out that the band gap of the semiconductor should be decreased at the metal-semiconductor interface as a result of the correlation between excitation in the semiconductor with the electrons in the nearby metal. In fact, he concludes that the band gap of the semiconductor may actually vanish for covalent semiconductors (typically with small band gaps). However, this effect extends only over a very small distance into the semiconductor ($\lesssim 1$ Å) and, hence, it is not at all clear that it plays an important role in determining the value of the barrier height.

More recently the theory (104, 105) has turned to trying to understand the precise role of the spatial arrangement of atoms at the metal-semiconductor interface. Mele & Joannopoulos (104) have studied the case of Al on GaAs. They conclude that in the initial stages of Al deposition on GaAs the Al replaces the surface Ga and the resulting Ga attaches to a surface As. In contrast, simple chemical considerations and quantum chemical calculations (J. J. Barton, C. A. Swarts, W. A.

Goddard, T. C. McGill, *J. Vac. Technol.* 17: In press) suggest that a single Al atom should bind to a surface Ga on a perfect GaAs (110) surface. The exchange reaction in which a surface Ga is replaced by an Al atom is exothermic but probably has a fairly substantial reaction barrier (of the order of a few eV) from the state of a Al bound to a surface Ga. These same considerations suggest that the reaction results in a Ga atom bound to the Al that has been incorporated into the GaAs (110) surface. Hence, at the present time the atomic positions for a small number of Al atoms on a perfect GaAs (110) surface is a subject of a great deal of discussion.

Following the suggestion by Spicer et al (107) that anion vacancies could be the origin of the states responsible for Fermi-level pinning on GaAs and InP, Daw & Smith (105) examined the position of the electronic levels for anion and cation vacancies as a function of spatial position from the surface. The calculations are carried out in the tight binding approximation; electron-electron interaction and lattice relaxation about the defect are neglected. They find that the position of the electronic levels of the vacancies are rather independent of the depth of the vacancy unless the vacancy is on the surface. Further, they find that in the case of both GaAs and InP the position of the Fermi level for a neutral vacancy is at the pinning position found experimentally both for metals (107) and oxides (107, 108). While these calculations for the simple vacancies may not be accurate enough to compare directly with the Fermi-level pinning position, they do suggest that defects may be the origin of the states responsible for the pinning.

In summary, a great deal of theoretical work has been directed at the metal-semiconductor interface. While this theoretical work has delineated many of the possible phenomena that can occur at the interface, we have not developed a complete microscopic picture of how important these various phenomena are in determining what occurs at a metal-semiconductor interface.

CONCLUDING REMARKS

The metal-semiconductor interface, or "Schottky barrier," in many respects resembles a p-n junction. In the latter, properties change smoothly and predictably across an interface. By contrast, the Schottky interface usually joins quite disparate substances, which makes prediction far more difficult. A principal characteristic of these structures, their barrier height, while subject to several experimental variables, can be made stable and reproducible. But what gives rise to the barrier?

Historically, correlations with other interfacial phenomena have been pursued, particularly work functions and thermodynamic properties.

The main effort to understand these structures today, however, is by direct study of the interface itself as it is being formed. Such studies are revealing in considerable detail the way interface states arise and are influencing theoretical treatments. The latter have focused on the covalent versus ionic character of the interface, but more recently are taking into account atomic arrangement at the interface and even structural defects.

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